**BLG222E**

**Computer Organization**

**Project 2**

**Group Number: 37**

**Group Members:**

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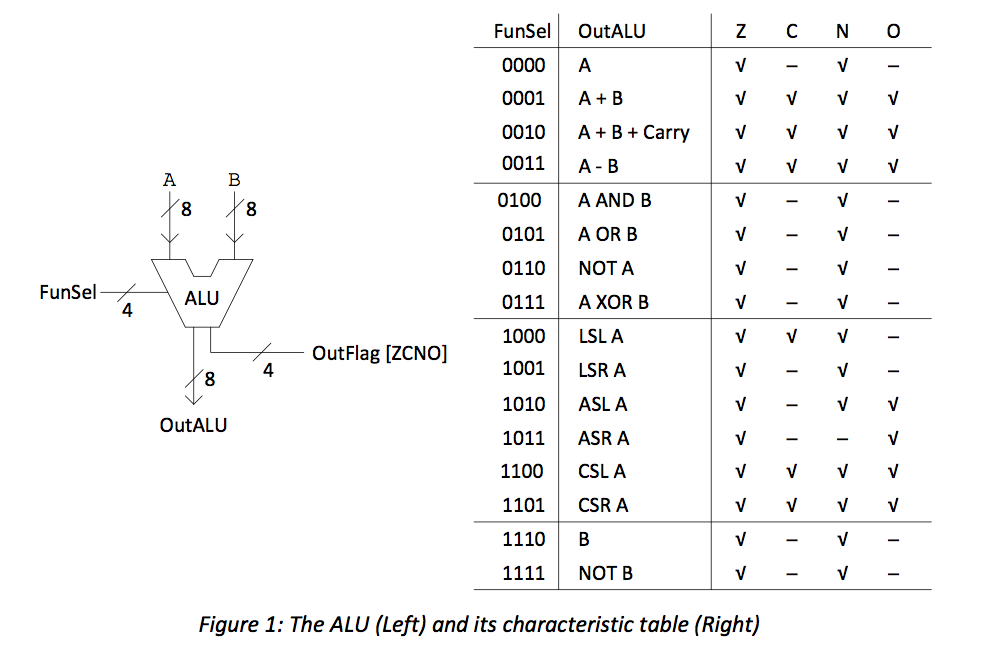
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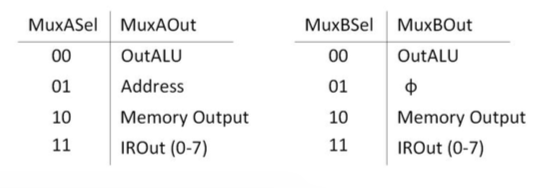
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**1 INTRODUCTION**

We designed a 8-bit input and 8-bit output Arithmetic Logic Unit which performs some arithmetic, logic and shifting operations with a register in this project according to the Figure 1.

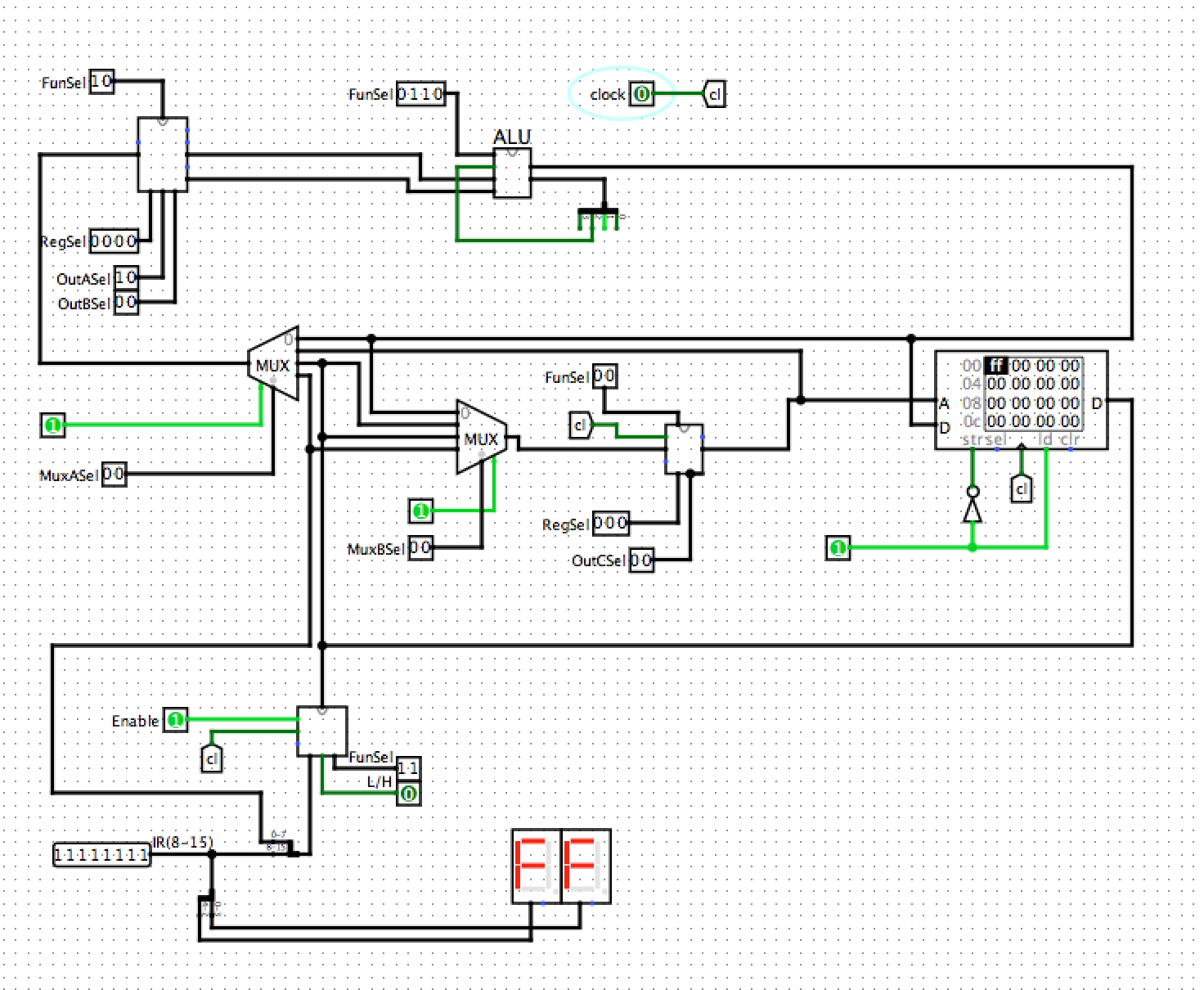
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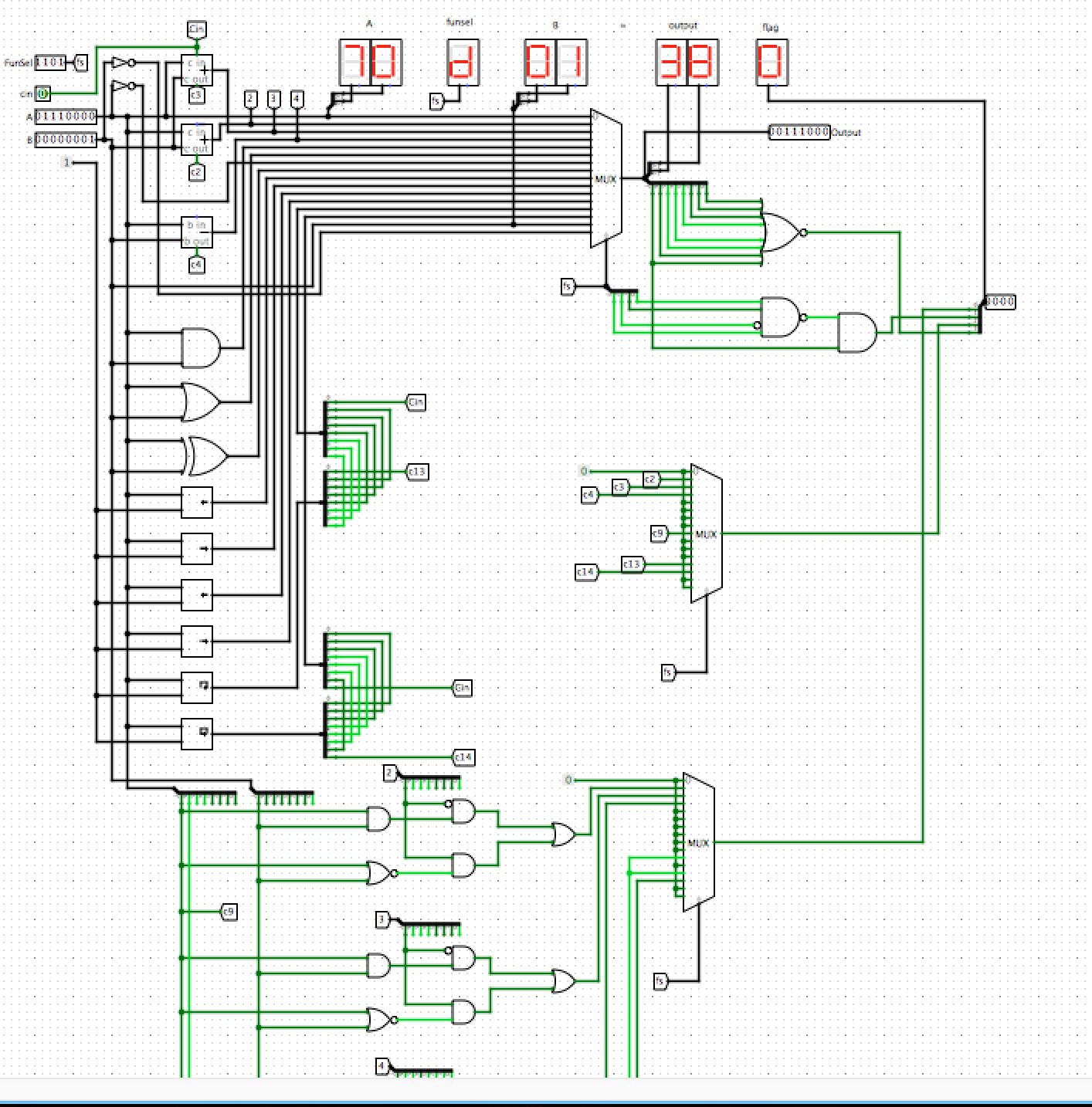
Then we designed an ALU System implementation according to this table.

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**2 CONCLUSION**

Those are our final circuits.

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**8 Bit - ALU**